

CLAIMS

1. A method of restoring faulty connections in an integrated circuit device having a plurality of memory circuit elements and a plurality of connecting pins connected to said memory circuit elements, the method comprising the steps of :

using an integrated circuit programming device, executing a device read command;

obtaining an error message from said programming device identifying certain of said connecting pins appearing to be disconnected from a memory circuit element; and

applying a voltage signal to said certain of said connecting pins.

2. The method in accordance with claim 1 wherein said signal comprises a read command for reading said memory device.

3. The method in accordance with claim 1 wherein said step of applying comprises applying said signal to said certain connecting pins through a resistor.

4. The method in accordance with claim 3 wherein said signal has value of at least 5 volts.

5. The method in accordance with claim 4 wherein said signal has value of negative five volts.

6. The method in accordance with claim 5 wherein said resistor has a value of 100 kilo-ohms.

7. A method of restoring faulty elements internal to an integrated circuit device having at least one external connecting pin, said method comprising the step of applying a voltage signal of a predefined level said external connecting pin.

::ODMA\PCDOCS\GRR\560847\1